Amendments to the Claims:

Revise the claims as set forth below. This listing of claims will replace all prior versions and listings, of claims in the application:

Listing of Claims:

1. (Previously presented) A method for detecting a monitor, the method comprising:

monitoring one pin of a connector coupled to a flat panel display;

asserting an output signal to indicate the one pin is in a first state; and

receiving the output signal at a display engine.

2. (Previously presented) The method of claim 1, wherein the output signal is an interrupt signal.

3. (Previously presented) The method of claim 2, wherein the interrupt signal is a system interrupt for a general purpose computer.

4. (Previously presented) The method of claim 6, wherein the output signal is stored in a register.

5. (Previously presented) The method of claim 1, further comprising determining if a voltage level of the one pin is in a stable state before asserting the output signal.

6. (Previously presented) The method of claim 5, wherein determining includes the voltage level of the one pin being stable when the input is stable for a predetermined amount of time.

7. (Canceled)

8. (Canceled)

9. (Canceled)

10. (Currently amended) The method of claim 1 further comprising ~~the step of~~:

operating in a normal mode of operation prior to monitoring, wherein the one pin is in a second state.

11. (Original) The method of claim 1, wherein the first state is indicative of a flat panel display being coupled to the connector.

12. (Original) The method of claim 1, wherein the first state is indicative of a flat panel display being decoupled from the connector.

13. (Currently amended) The method of claim 1 further ~~comprising~~ comprising:

driving the flat panel display from a flat panel display engine in response to asserting the first output signal.

14.-22. (Canceled)

23. (Previously presented) A system for providing a display image to a flat panel monitor, the system comprising:

a processing module; and

memory operably coupled to the processing module, wherein in the memory stores operational instructions that cause the processing module to:

monitor one pin of a connector coupled to a flat panel display;

assert a output signal to indicate the one pin is in a first state; and

receive the output signal at a display engine.

24. (Previously presented) The system of claim 23 wherein the output signal is a system interrupt signal for a general purpose computer.

25. (Currently amended) A method for detecting a monitor, the method comprising:

providing display information to a first display;

determining when an external flat panel display becomes available, by monitoring at least one pin of a connector coupled to a flat panel display;

asserting an output signal to indicate the pin is in a first state;

providing an interrupt signal in response to the asserted output signal; and[[;]]

providing display information to the external flat panel display in response to the interrupt signal.

26. (Previously presented) The method of claim 25 further including determining if an interrupt enable signal is activated and if so providing the interrupt signal.

27. (Previously presented) The method of claim 25 including determining if a voltage level of the first pin of the connector coupled to flat panel display is in a stable state before asserting the output signal.